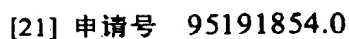


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Figure 1 is a block diagram of a system architecture. The diagram includes the following components and connections:

- System Function (系统功能)** (50) and **ROM-R-EPROM** (51) are at the top left.
- Closing Control (合闸控制)** (56) and **D-Ctrl** (57) are at the top right.
- Support Software SW (支持软件 SW)** (53) is in the center.
- Receive/Transmit (接收/发送)** (54) is below the support software, with **Receive Data (收数据)** (55) and **Transmit Data (发数据)** (58) below it.
- CPU** (59) is to the right of the receive/transmit block.
- CPU Application (CPU 应用)** (62) is at the bottom left, with **CPU-应用** (63) and **CPU Data (CPU 数据)** (64) below it.
- LAMP Application (LAMP 应用)** (65) is at the bottom right, with **LAMP-应用** (66), **TCP/IP Communication (TCP/IP 通信)** (67), and **UDP/IP Communication (UDP/IP 通信)** (68) below it.

Connections (indicated by arrows):

- From **System Function** to **Support Software SW**.
- From **Support Software SW** to **Receive/Transmit**, **CPU**, and **LAMP Application**.
- From **Receive/Transmit** to **CPU Application**.
- From **CPU** to **CPU Application**.
- From **CPU Application** to **Receive/Transmit**.
- From **CPU Application** to **LAMP Application**.
- From **LAMP Application** to **Receive/Transmit**.
- From **LAMP Application** to **TCP/IP Communication** and **UDP/IP Communication**.
- From **TCP/IP Communication** to **TCP/IP Communication** and **UDP/IP Communication**.
- From **UDP/IP Communication** to **TCP/IP Communication** and **UDP/IP Communication**.

Labels 50 through 70 are placed near the corresponding blocks.